

RFX 881

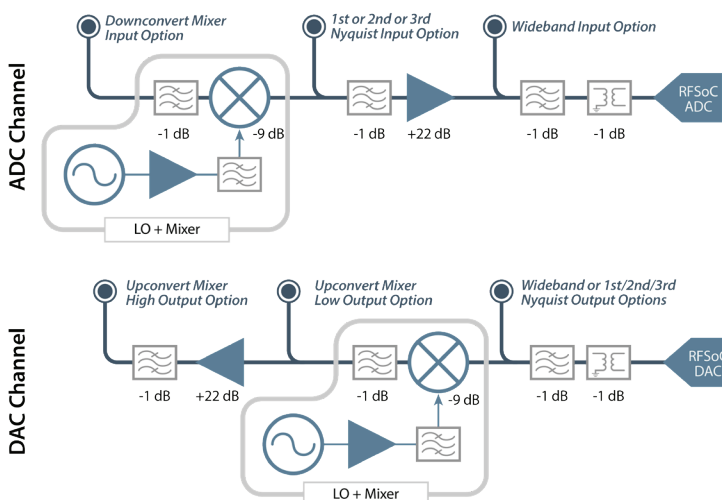
RFSoc Ethernet Card

ADC Front End Options

The RFSoc chip at 5 GSPS offers a 2.5 GHz bandwidth at up to 6 GHz. Aliasing challenges require filtering in the path. The RFX 881's "Wideband" configuration targets customers willing to provide all filtering and amplification external to the card. BittWare provides a DC block and a 6 GHz LPF. The RFX 881 also offers build options for on-card bandpass filtering and amplification with filters available for Nyquist 1, 2 and most of 3. BittWare also offers the RFX 881 with a mixer and LO for up and down conversion up to 35 GHz.

How the ADC/DAC Configuration Options Work

Each card is factory-configured for one set of input or output options per ADC/DAC channel



Included IP

BittWare RFX cards ship with an application that converts between analog data and timestamped, VITA-49.2 packets. It works in both ADC and DAC directions. The application also transfers raw data between the RFSoc and the ARM memory or eMMC. The RFSoc's ARM Processing System runs Linux and acts as a control plane. More specifically, BittWare has integrated card management functions through customized Linux drivers. BittWare has also ported AMD's RF Tool application, a socket library that configures an RFSoc chip. Above both Linux and the RF Tool, BittWare provides its own RFUtils commands and examples. RFUtils can generate synthetic waveforms as well as load and stream waveforms from files. All of this is fully supported by BittWare and we provide source code.

The RFX 880 application passes packets in Ethernet frames through two 100 GbE ports and uses Aurora protocol for the other two ports.

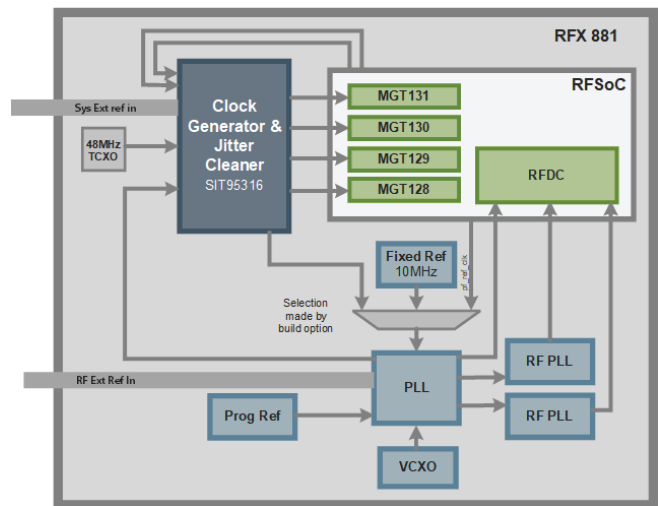
BittWare also supplies an unsupported port of AMD's RF Analyzer which we use to help characterize our cards.

Analog Characterization Data

BittWare fully tests and documents the analog characteristics of each RFX 881 configuration, be it wideband or amplified, with 8 ADC channels or 8 DAC channels (to mention the extreme possibilities). That data is available for each significant printed circuit board (PCB) revision, roughly one month after a new PCB passes through manufacturing. The reports are available to customers through developer.bittware.com.

Clock Tree

The RFX 881 card offers a very flexible clock tree. It can accommodate reference clock inputs from Synchronous Ethernet, eCPRI, CPRI, as well as reference clocks fed in directly over coax cables. The default card configuration also has a 10 MHz OCXO and a 125 MHz fixed VCXO. Some of the RFX 881 clock tree flexibility is established through build options when the card is manufactured. BittWare will configure optimized cards for each customer, with a minimum quantity requirement in each order.



Customize the ADC and DAC count

The ZU47DR chip has eight ADC channels and eight DAC channels. However, the RFX 881 front panel only has room for eight coax channel connections. BittWare designed an analog signal path that can, as a build option, operate as either an ADC or a DAC. This flexibility allows BittWare to offer RFX 881 variants with any number of ADC or DAC channels, provided the total number does not exceed eight.

Chassis Options

BittWare offers a WaveBox Gamma chassis that holds up to three RFX 881 cards in 1U. There is no motherboard inside. Alternatively, if you want to control the RFX 881 over PCIe, BittWare offers its WaveBox Sigma chassis. Both versions of WaveBox require an RFX 881 that isn't as wide as a standard, dual-slot PCIe card. If you wish to provide your own chassis, you can order the RFX 881 built a little wider to fit into a generic server's PCIe backplane.



WaveBox

RFX 881

RFSoc Ethernet Card

Board Specifications

RFSoc	<ul style="list-style-type: none">• AMD Zynq UltraScale+ RFSoc<ul style="list-style-type: none">• ZU47DR (G1517package), core speed grade -2• Contact BittWare about ZU43DR RFSoc card configuration
Processing Subsystem (PS)	<ul style="list-style-type: none">• Application Processing Unit Quad-core Arm Cortex-A53 MPCore at 1.2 GHz• Real-Time Processing Unit Dual-core Arm Cortex-R5F MPCore at 525 MHz
Analog	<p><i>Default build using ZU43DR channel count</i></p> <ul style="list-style-type: none">• 4x 5 GSPS 14-bit ADCs<ul style="list-style-type: none">• Wideband build option provides -1 to -28 dB gain from 50 MHz to 6 GHz with a 6 GHz LPF• 1st Nyquist (L-Band) build option provides +22 to -27 dB on all 4 channels, constrained between 500 MHz and 2.5 GHz by filters FL1 and FL2• 2nd and 3rd Nyquist filters are also available on amplified channels as a special order• Max input power: 0 dBm on the amplified version and +15 dBm on the wideband• 4x 9.7 GSPS 14-bit DACs<ul style="list-style-type: none">• Range of +3 to -24 dBm (high power end of those ranges is a function of frequency)• With the amplifier, +20 to -24 dBm• Constrained by a 4.8 GHz LPF• Balun used in both ADC and DAC channels begins its frequency roll-off at 8 GHz• Programmable clocks• External reference and triggers• Push-on SMPM connectors with optional SMA pigtails
On-board flash	<ul style="list-style-type: none">• PS connects to a pair of 2Gb QSPI parts configured in "Dual Parallel" mode. The PS boots from the QSPI to configure itself, then pulls the logic bitstream from the QSPI and configures the PL• 32GB eMMC for ARM disk
External memory	<ul style="list-style-type: none">• Processing system: 16GB, 64-bit DDR4-2400 with ECC• Programmable logic: 32GB, 2x banks 64-bit DDR4-2666 with ECC (our example uses it as 36GB no ECC, 72-bit)
External digital interfaces	<ul style="list-style-type: none">• Processing system<ul style="list-style-type: none">• PCIe Gen2 x4• Dual RJ45 Ethernet, one on the front panel, second inside the chassis• Single USB for UART and JTAG, dual connectors. One on the front panel, second inside the chassis.• Programmable logic:<ul style="list-style-type: none">• Up to 400 Gb/s available via front panel 2x QSFP-DD• AMD Hard IP support for dual 100GbE

Cooling	<ul style="list-style-type: none">• Standard: double-width passive heatsink• Contact BittWare for other cooling options
Electrical	<ul style="list-style-type: none">• On-board power derived from 6-pin AUX connector• Power dissipation is application dependent• Typical max power consumption 50W
Environmental	<ul style="list-style-type: none">• Designed to operate between +5C and +40C chassis air inlet temperature, noncondensing (air temperature inside the chassis between +5C and +60C)
Quality	<ul style="list-style-type: none">• Manufactured to IPC-A-610 Class 2• RoHS compliant• CE, FCC, UKCA & ICES approvals
Form factor	<ul style="list-style-type: none">• ¾-length, standard-height PCIe dual-slot card (x16 mechanical)• Supports standalone operation• BittWare's WaveBox Gamma chassis was specifically designed to house RFX 881 cards

Development Tools

Firmware development	Bittware provides interface software support for an example bitstream that fully configures the RFSoc chip using a scripting language running on the RFSoc's ARM. AMD Vivado development tools are fully supported for development of custom designs.
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Deliverables

- RFX 881 RFSoc Transceiver
- Data capture and relay example - Full source code
- 1-year hardware warranty

To learn more, visit www.BittWare.com

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